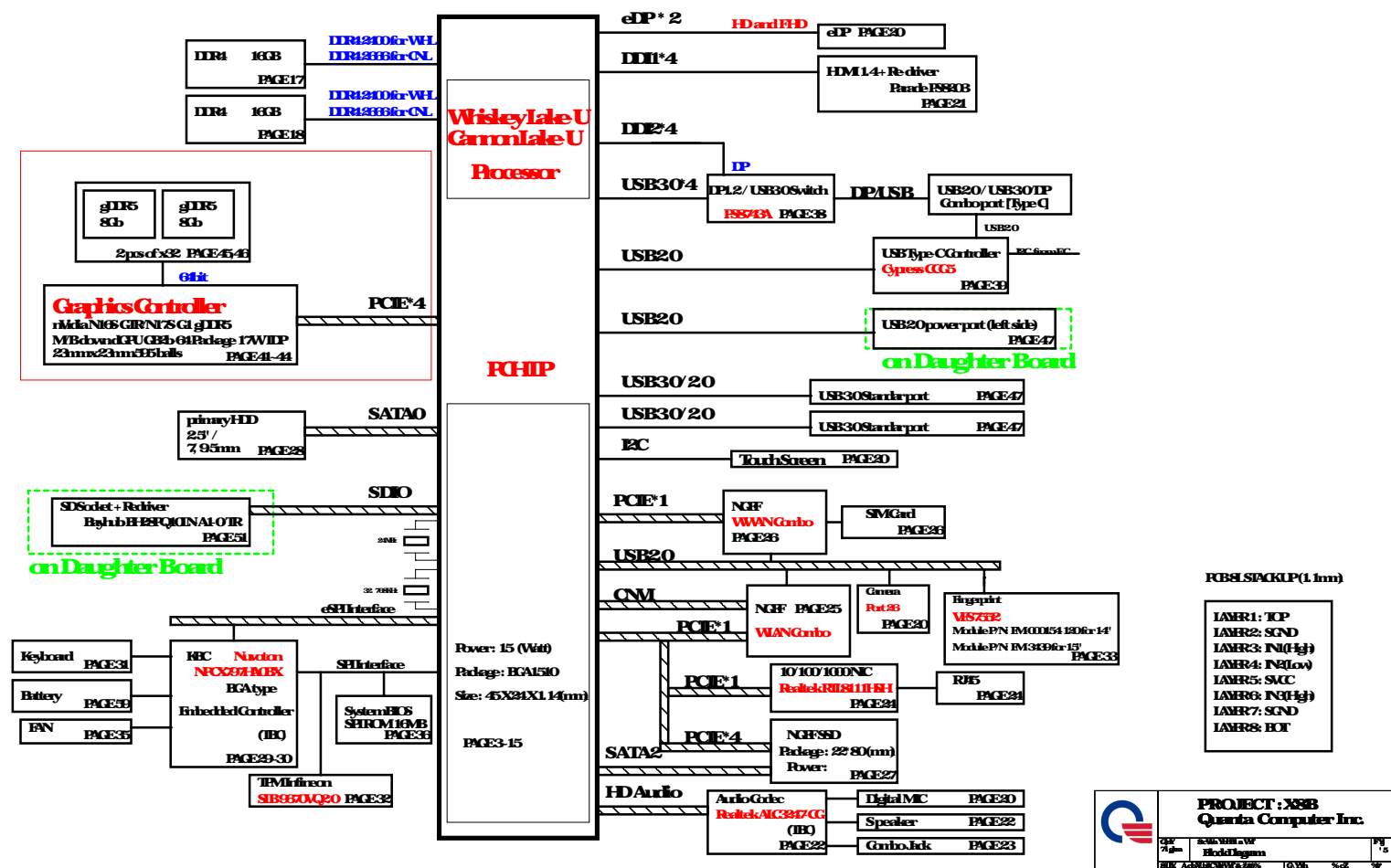


**On**

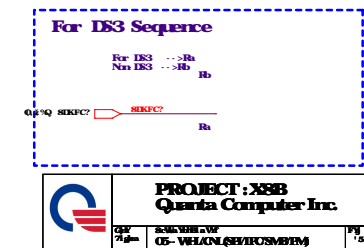
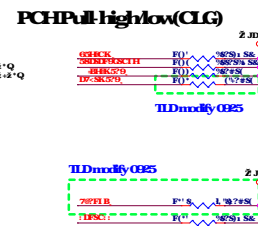
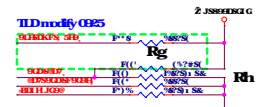
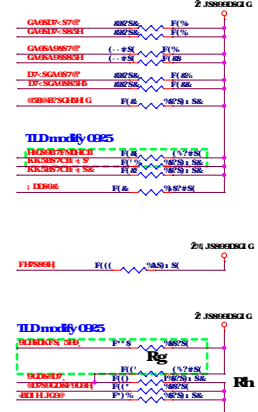


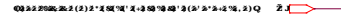


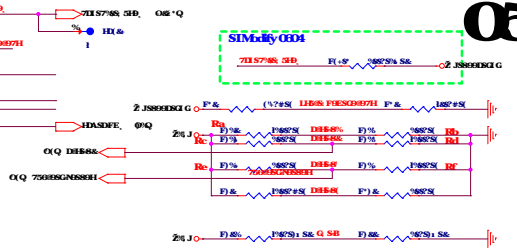
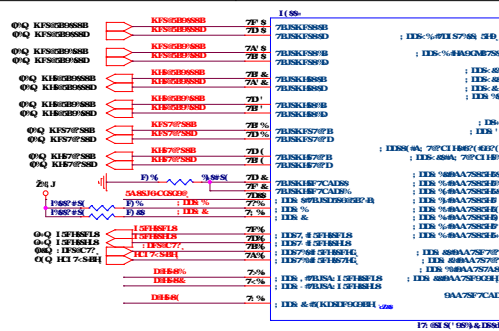
6	
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6	
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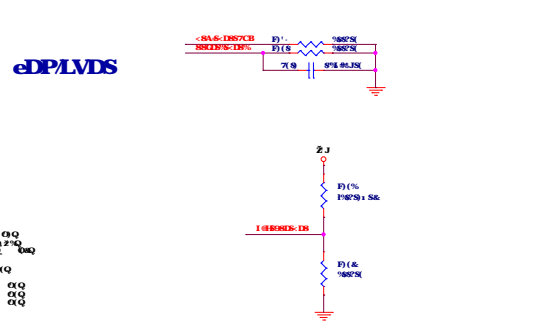
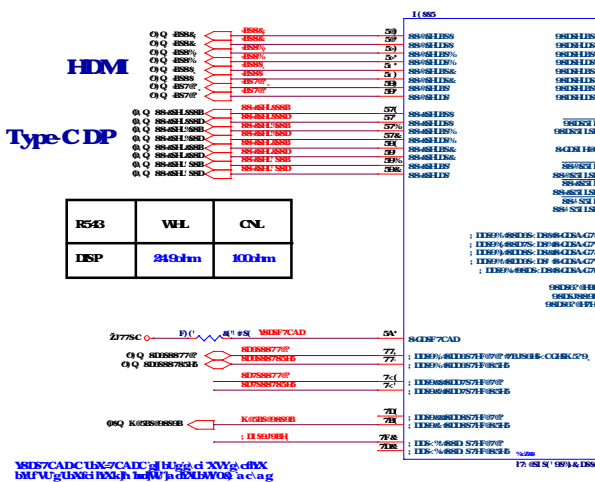
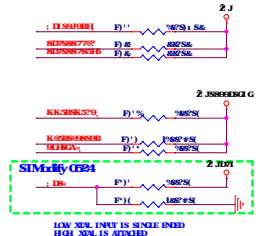
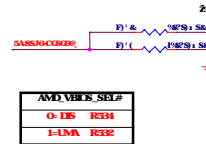




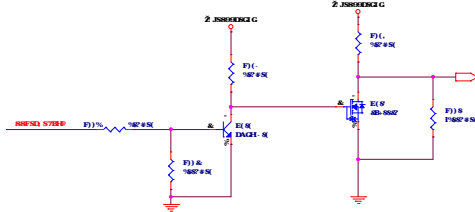
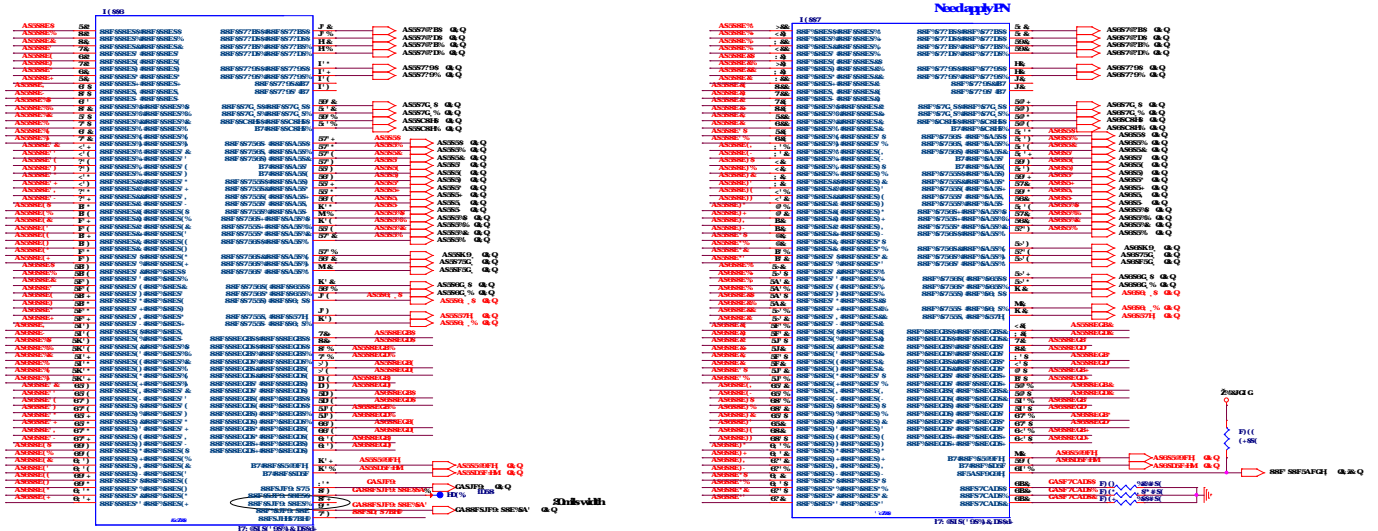
PLT ID1	PLT ID2	PLT ID3		SG, IN
Ra	Rc	Ro	H	DS-1 RS9 (X-axis)
Rb	Rd	Rf	L	UMA-O RS92
O	O	O	E33 <sup>1</sup> UMa	
O	1	1	14 <sup>1</sup>	
O	1	O	15G <sup>1</sup>	
O	O	1	17	

**LOW\_FREQ\_SELECT**

LOW 38400Hz采样  
HIGH 20480Hz采样



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	VHL	CNL
RCOMP0	150nm	100nm
RCOMP1	800nm	100nm
RCOMP2	100nm	100nm

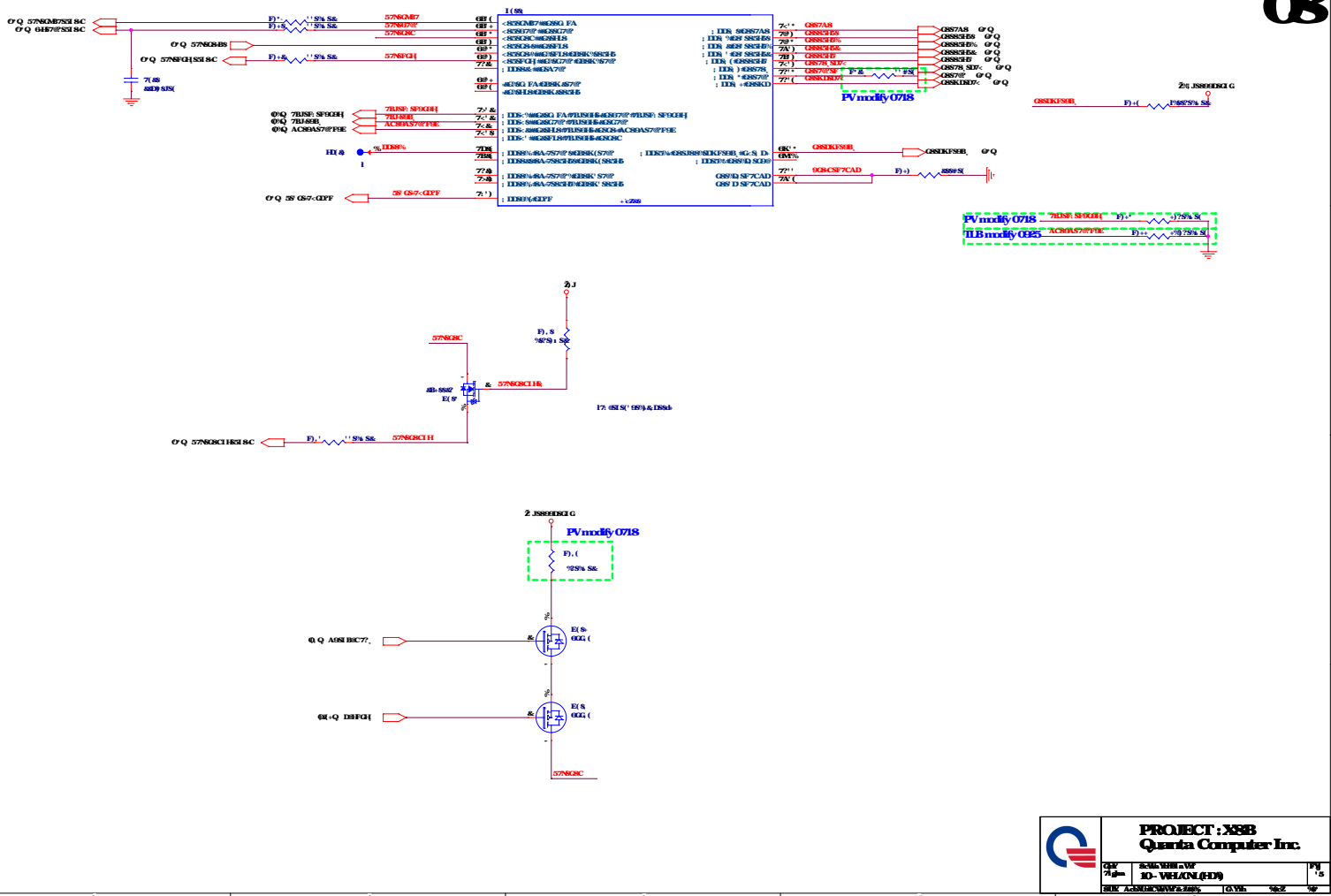


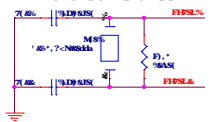
**PROJECT : XBB**  
**Quanta Computer Inc.**

DATE: 2018/08/08  
 BY: 08-VH/AN (TDR/AN)  
 FOR: 08-VH/AN (TDR/AN)

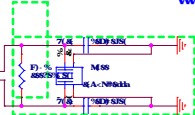






**RTC Clock 32768KHz**

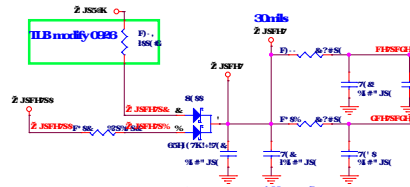
## PVMbdfy 0727



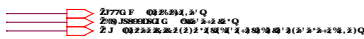
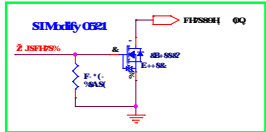
## SIMbdfy 0521



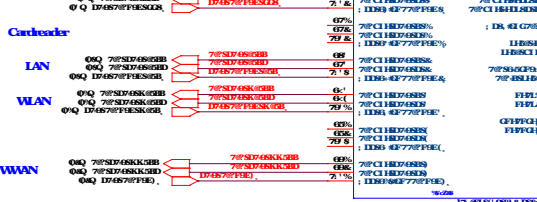
### RTC Circuitry(RTC)



**RTC Power trace width 20mils.**



## SSD

**Cardreader**

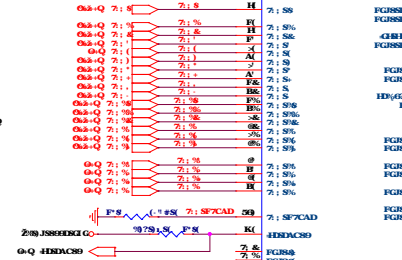
## LAN

## REFERENCES

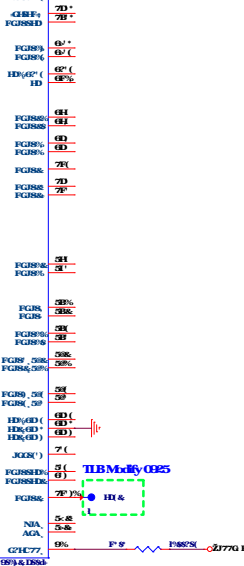
## WAN

## SIMbify 0530

**CFD 19 need Reserve TP**

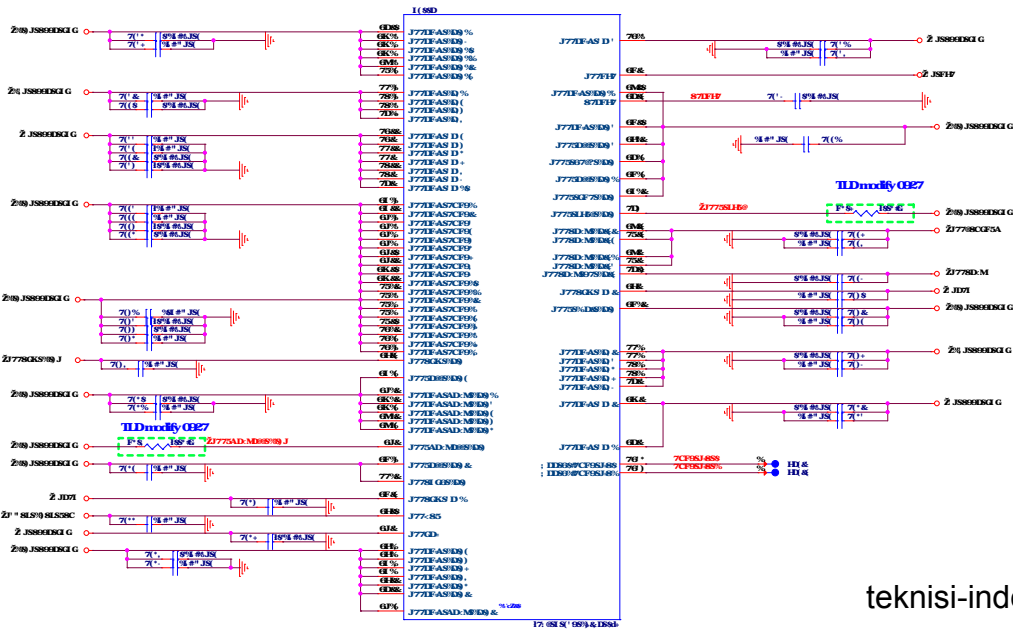


PGJSSHD  
PGJSSHD

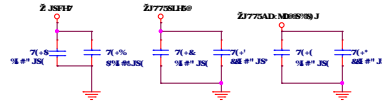
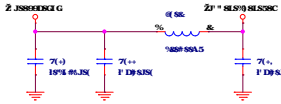
**CLK\_REQStrap Pin(CLG) 2 J**

**PROJECT : X88B**  
**Quanta Computer Inc.**

Q17	2-Methyl-2H	Pg	15
7	11-VH/CN(CIKHS/BC)		
8	AcCN/CN(CIKHS/BC)	Q18	16

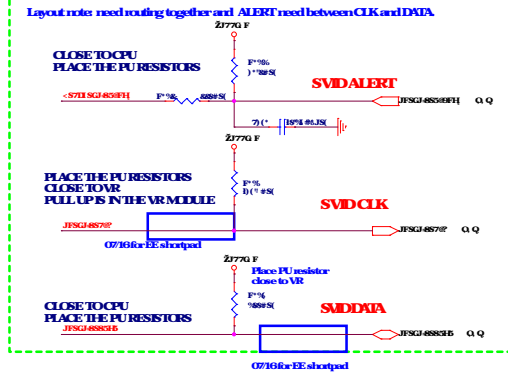
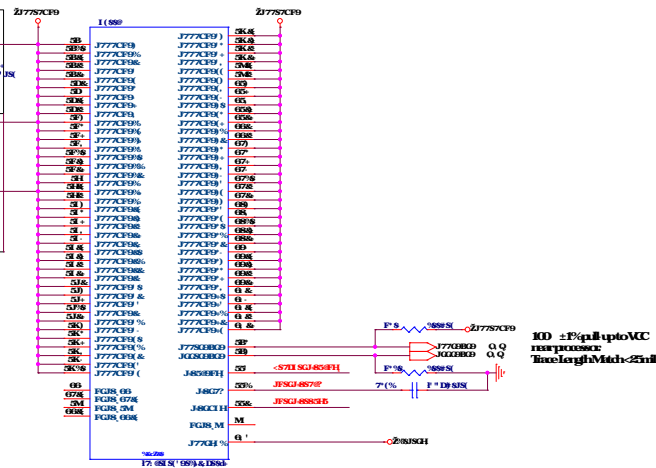
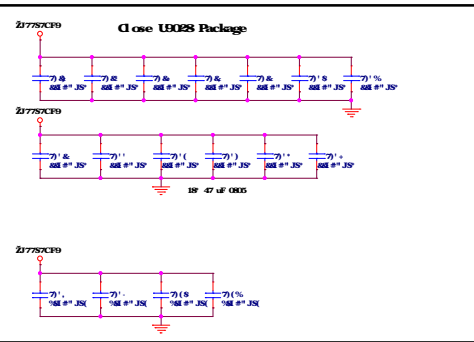
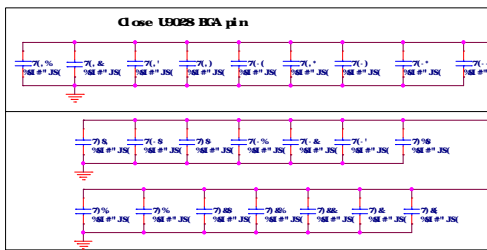


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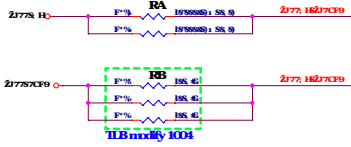


2.000V rail (1000uF) 1000uF  
2.500V rail (1000uF) 1000uF  
2.000V rail (1000uF) 1000uF  
2.500V rail (1000uF) 1000uF

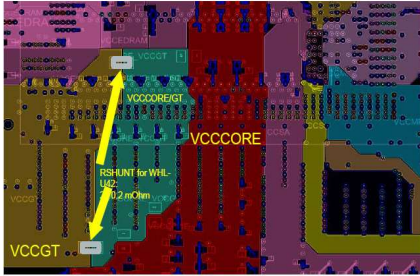
PROJECT : X86			
Quanta Computer Inc.			
REV	REV	REV	REV
1.0	1.0	1.0	1.0



	WHL-U42ES1 CNL-U22	WHL-U42ES2
RA	STUFF	N
RB	N	STUFF

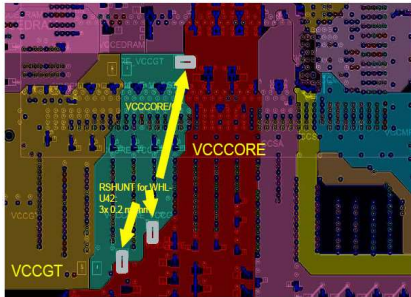


Routing guideline for RSHUNT placement for WHL ES1, CFL43e and CNLU22

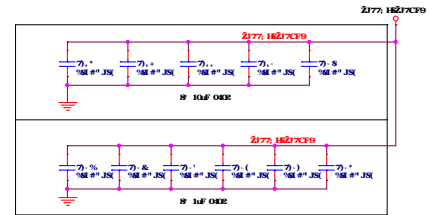
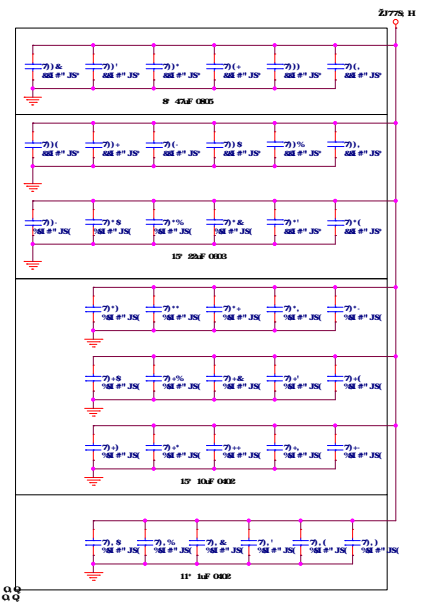
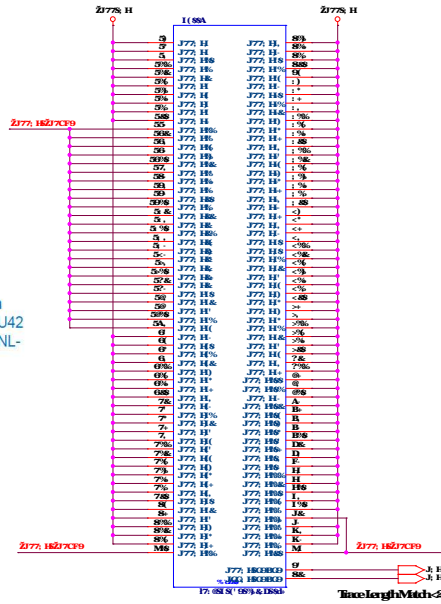


Place 2x 0.2 mOhm  
RSHUNT for WHL-U42  
(ES1)/CFL-U43e/CNL-  
U22

Routing guideline for RSHUNT placement for WHL ES2



Place 3x 0.2 mOhm  
RSHUNT for WHL-  
U42 (ES2)



	PROJECT : X8B	
	Quanta Computer Inc.	
DESIGNED BY	DESIGNED BY	PN
REV	REV	REV



[illegible]

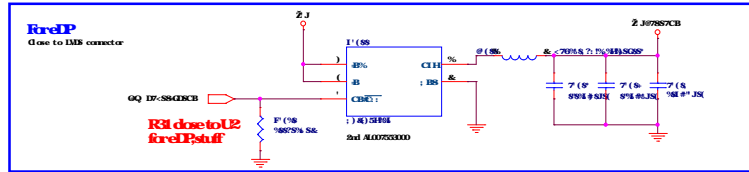
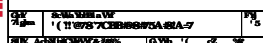




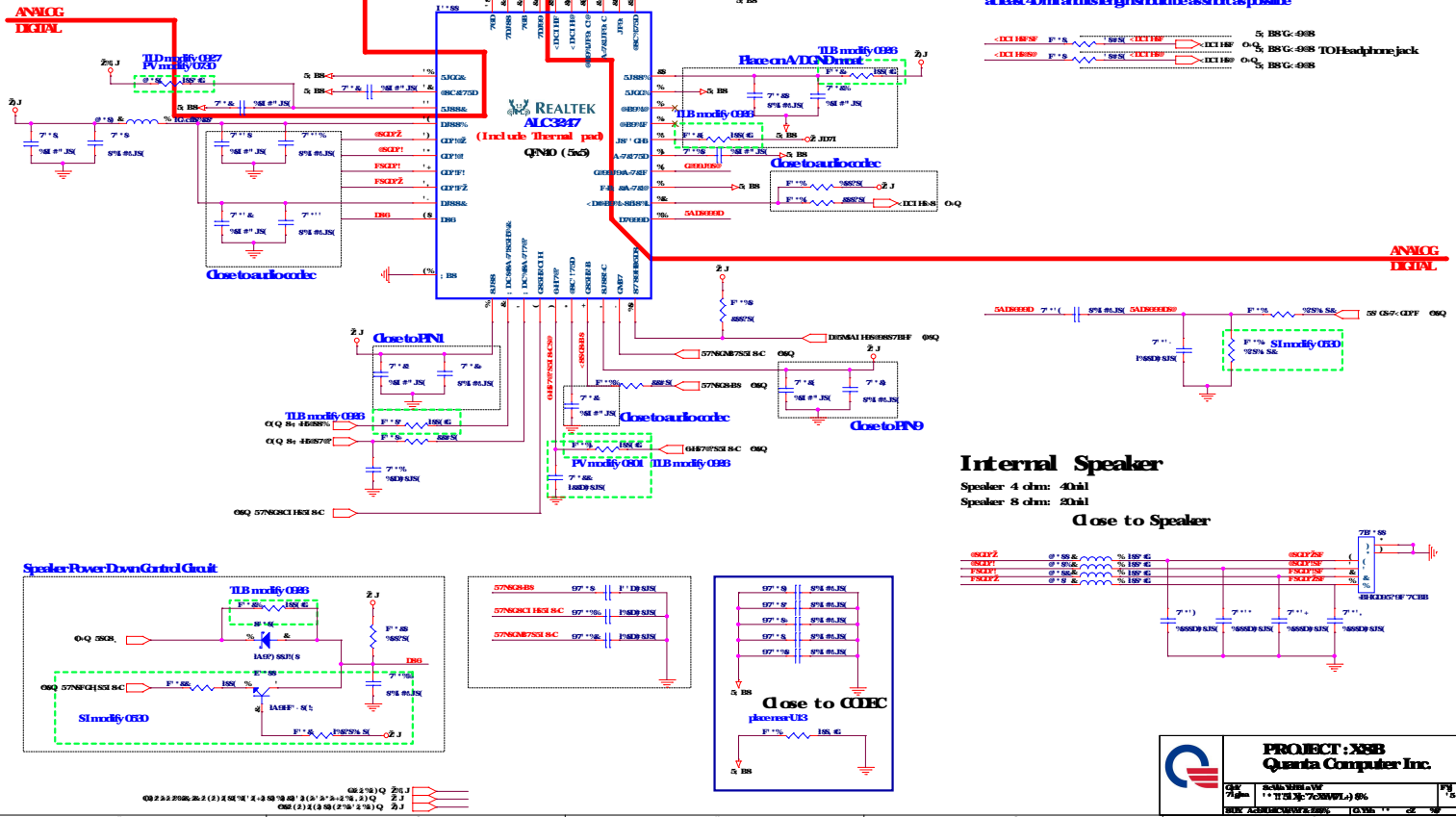


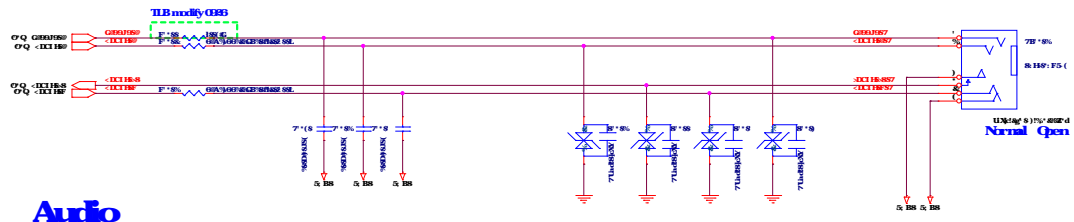




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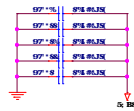






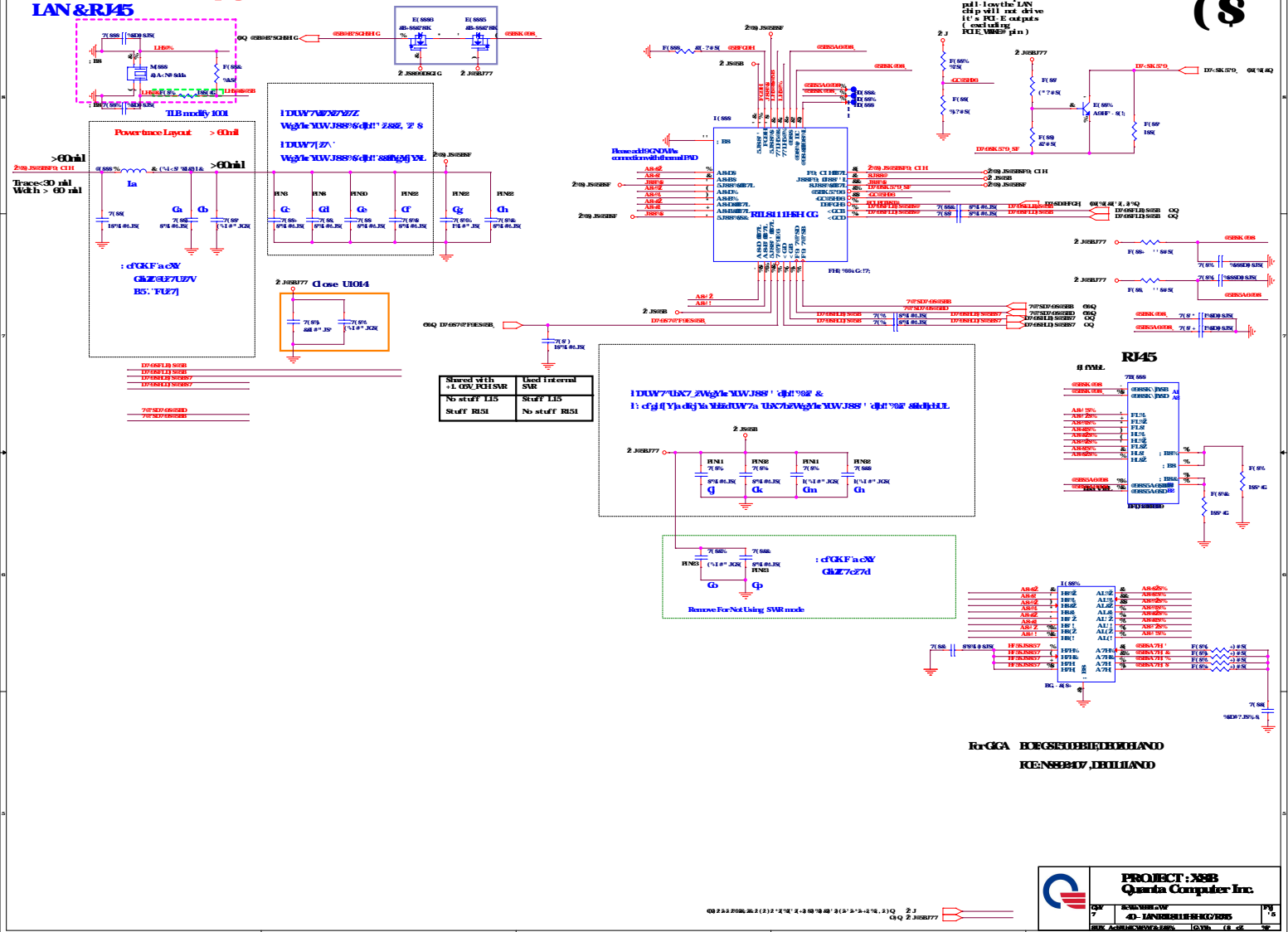
## Audio

**All Clamp Diode need  
close to ADO Jack**

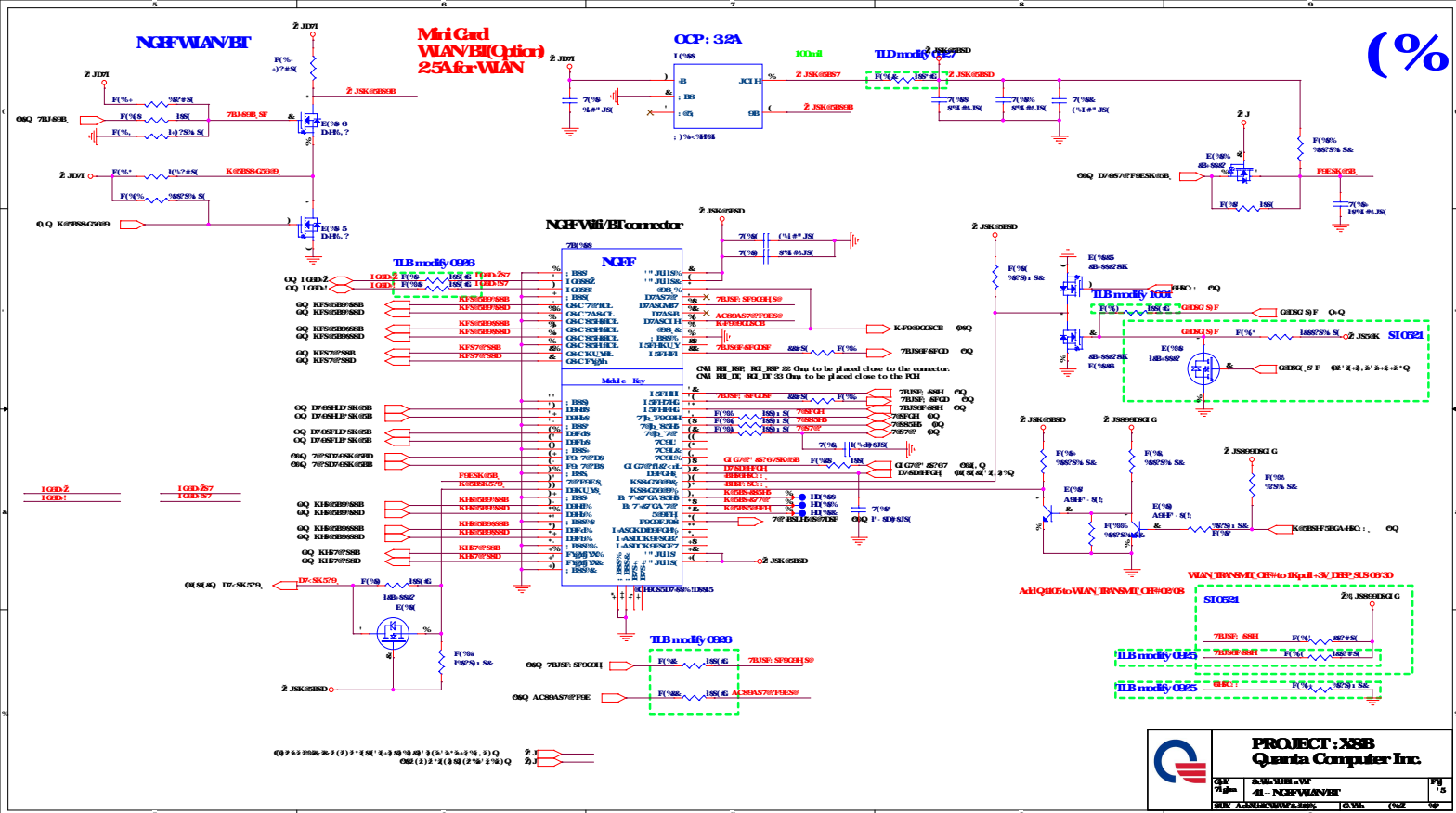


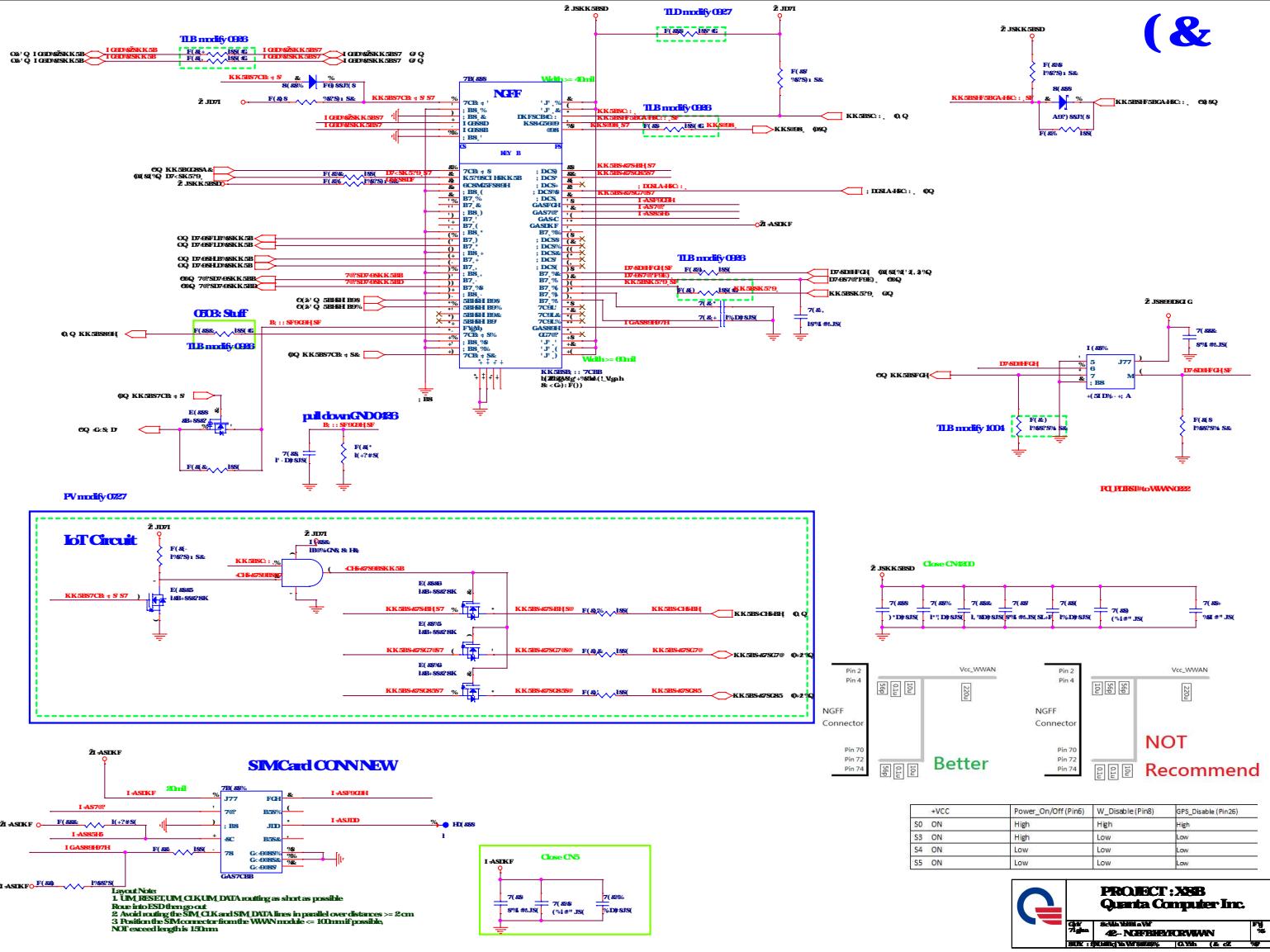
**PROJECT : X86B**  
**Quanta Computer Inc.**

Q47	2-10-1981	11:58 AM	57?	15
SUBJ	ACHIEVEMENT & ZEPHYRUS	Q47	11:58 AM	57?







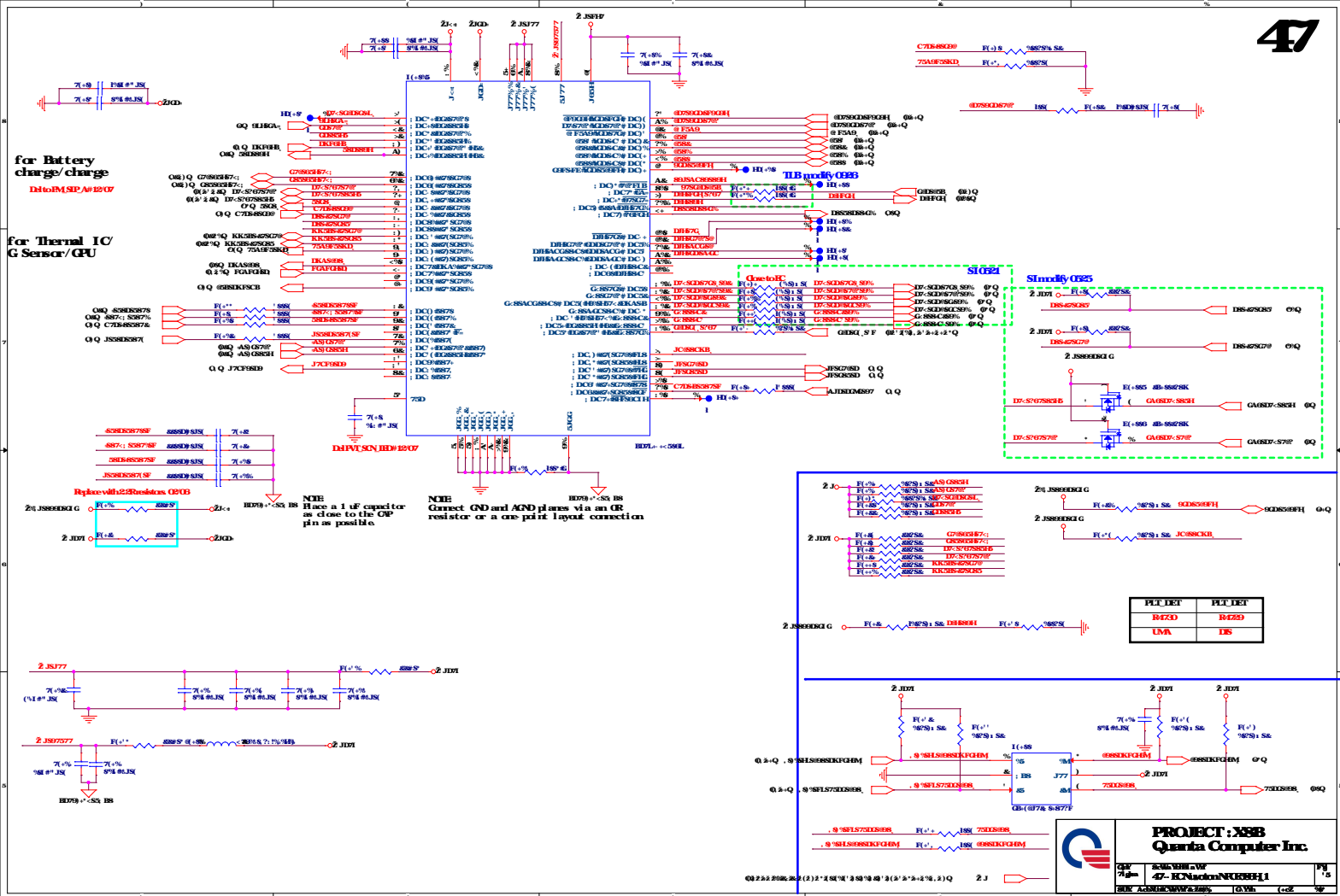




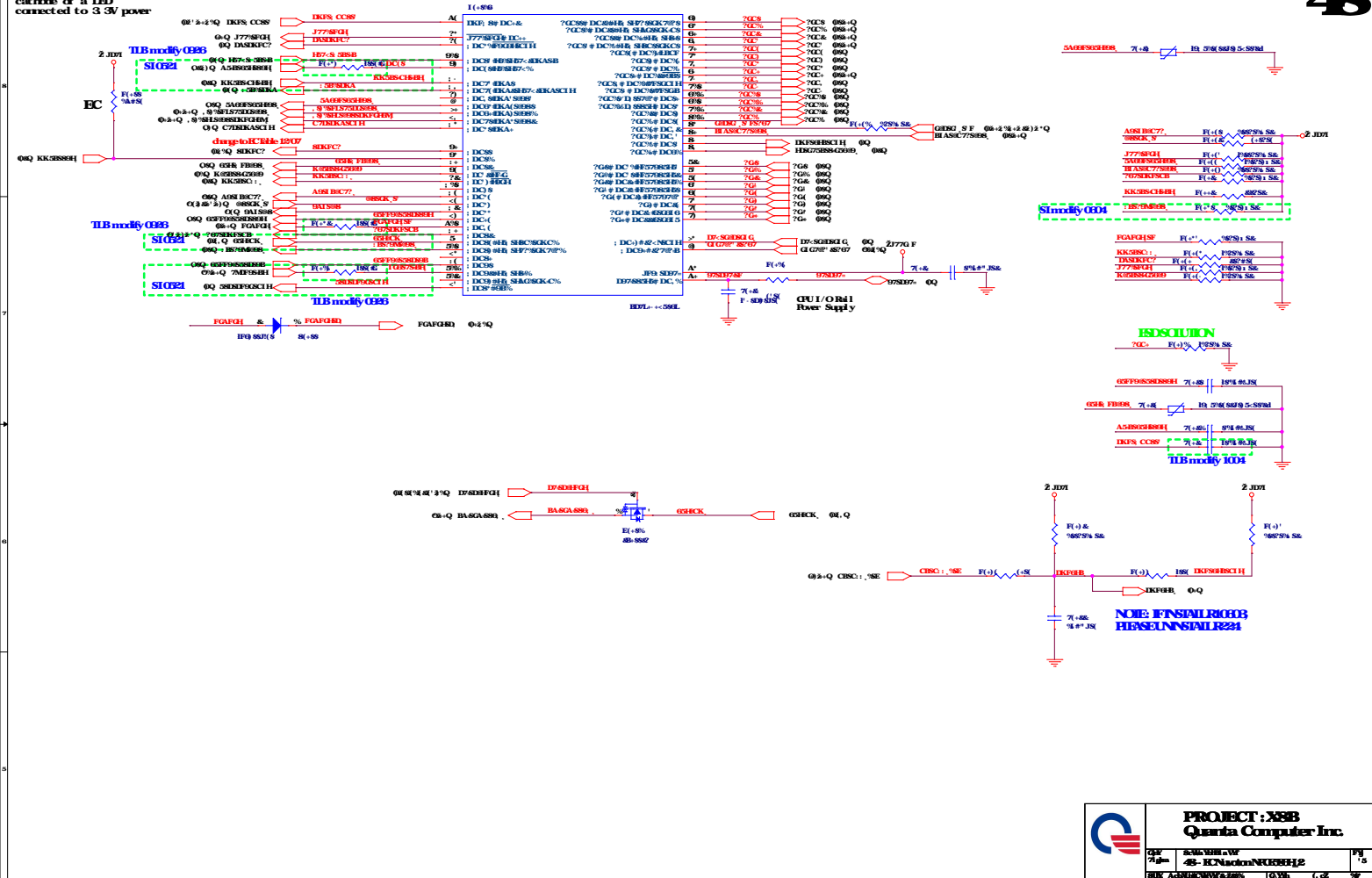


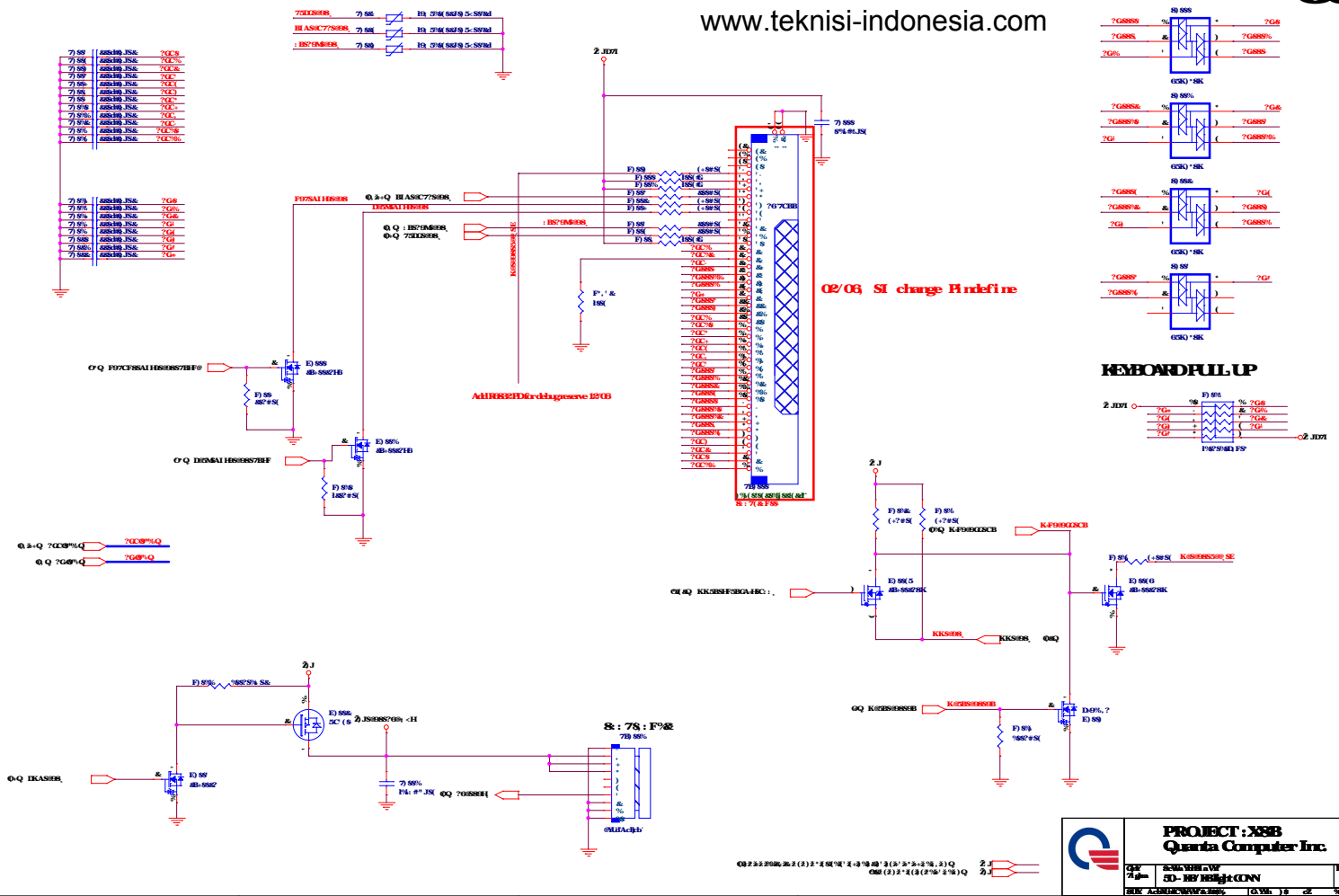
for Battery  
charge/charge

for Thermal IC/  
G Sensor/GPU



48



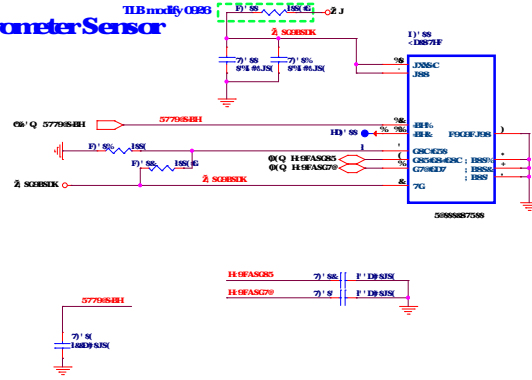


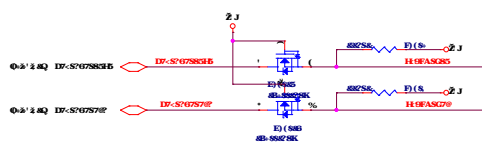







## Accelerometer Sensor

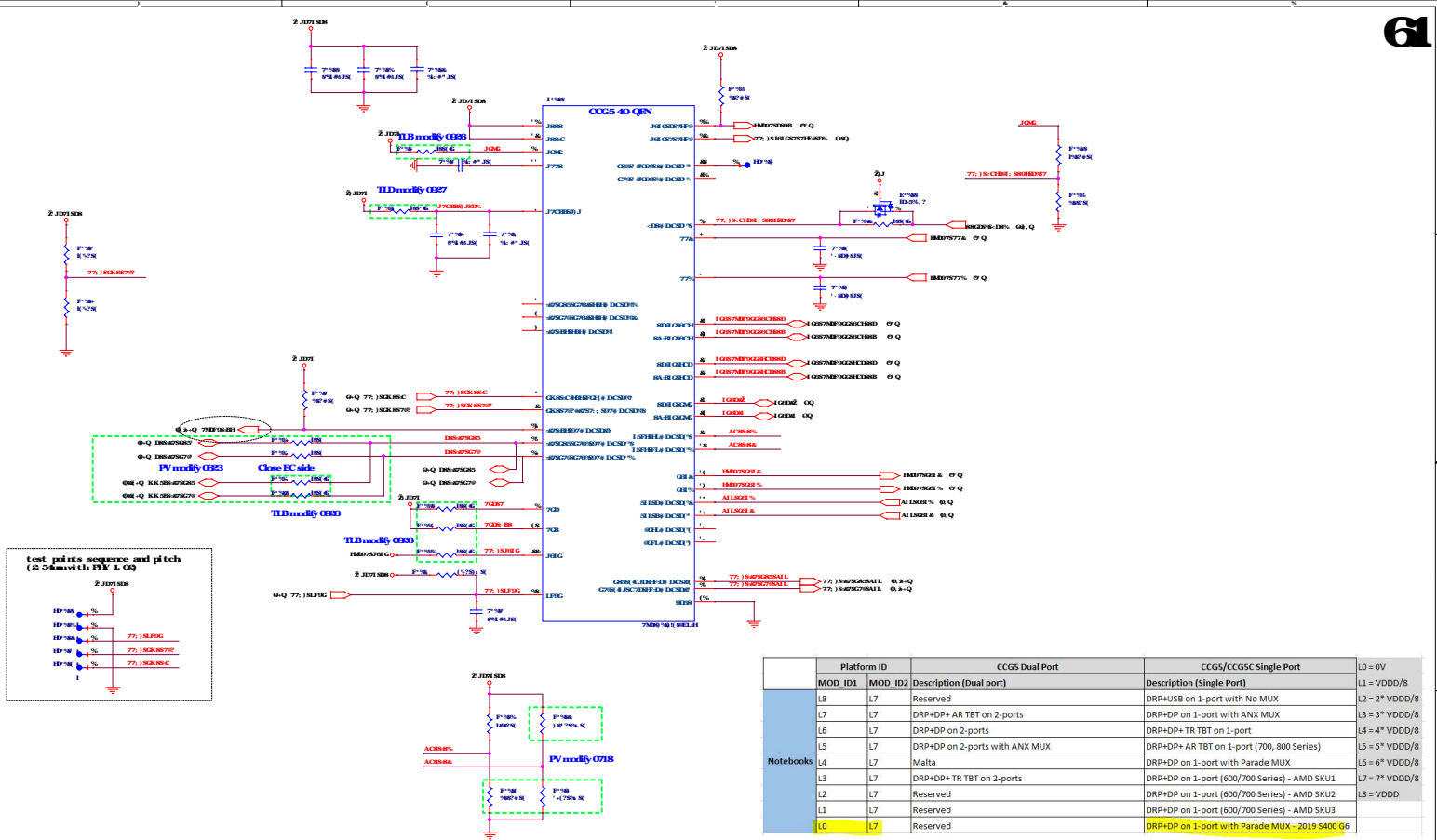
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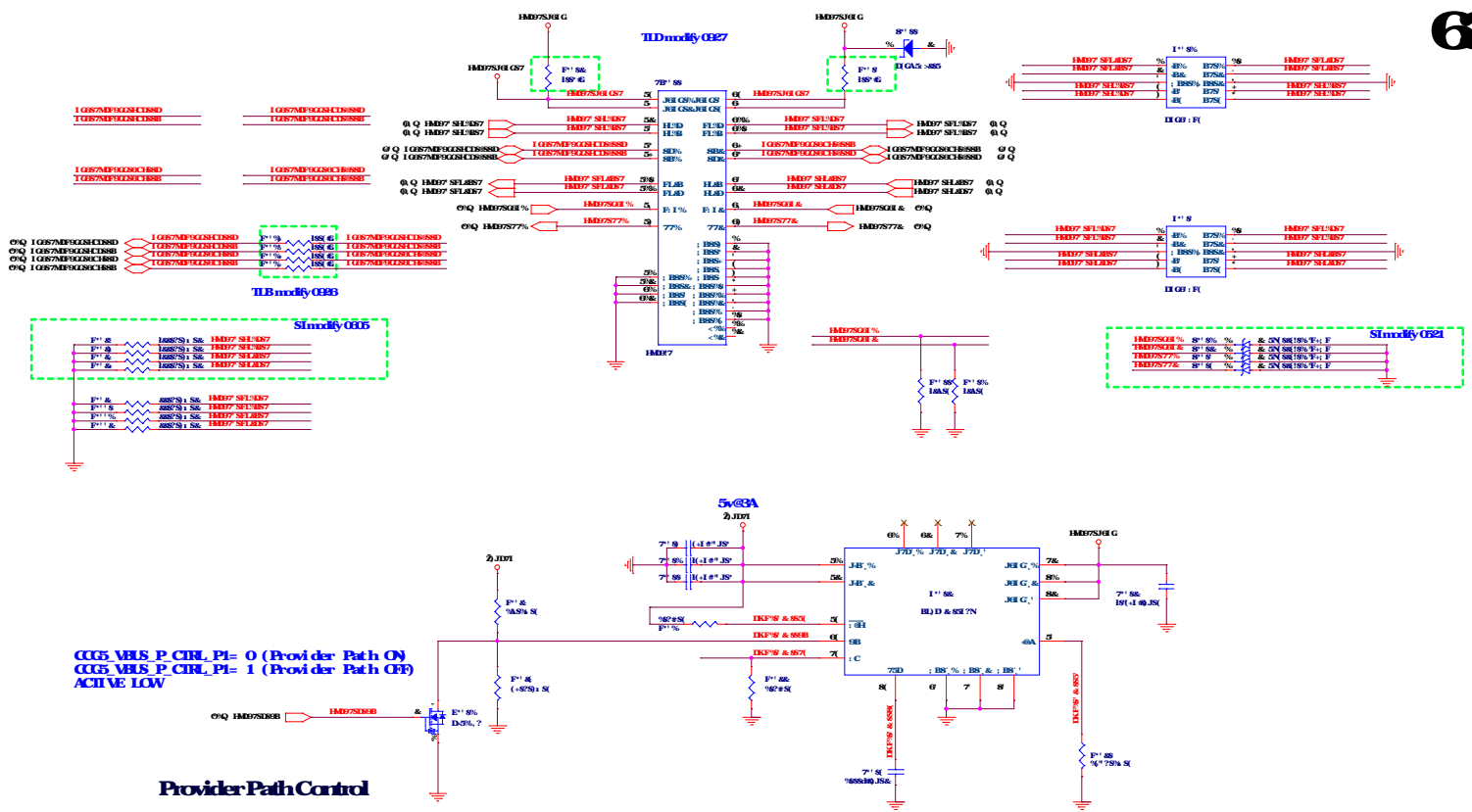
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	QSB 74-01	88-06-01-01-01-01 <b>3B - High-Performance</b>	P1 5
NEW Acoustic Wave & Amp, (C.V.M.) 1" x 1" x 1"			

	<b>PROJECT : XBB</b>	
	<b>Quanta Computer Inc.</b>	
	DATE: 07-10-2008	BY: J. L. S. S. S.

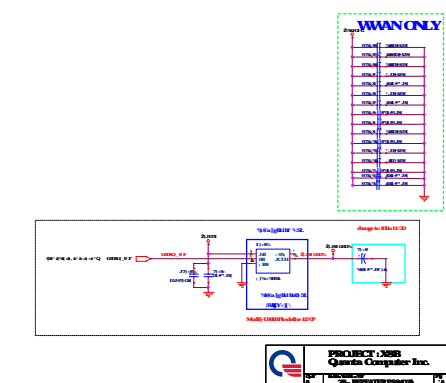
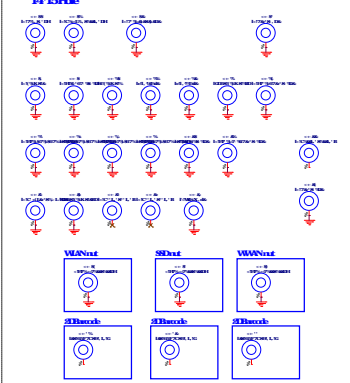
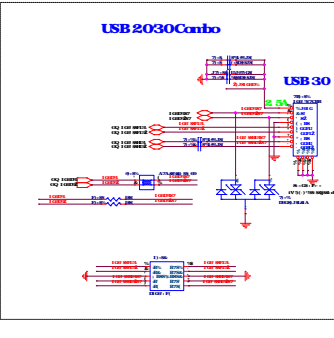
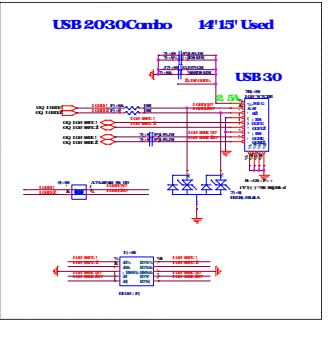
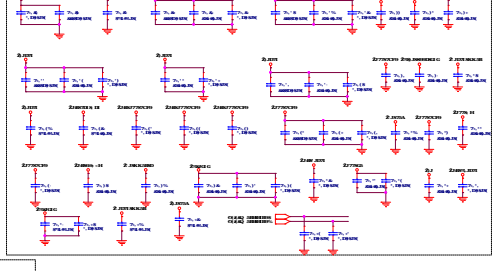
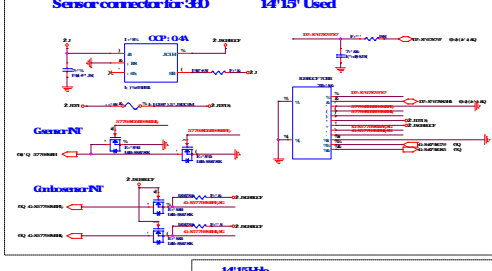
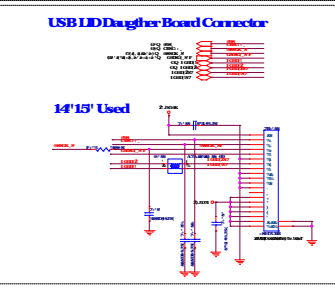
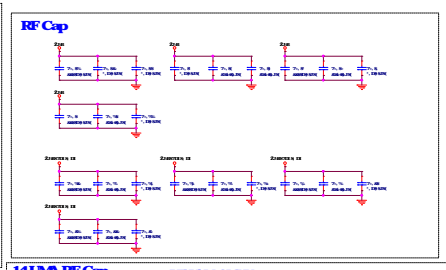
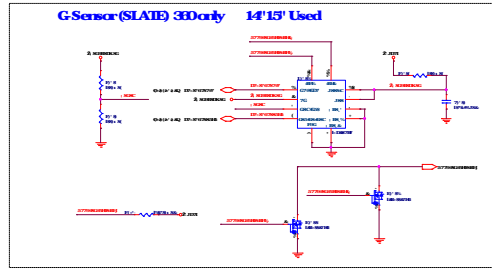
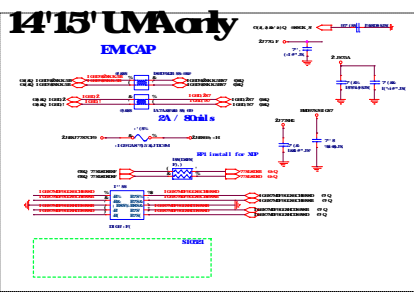


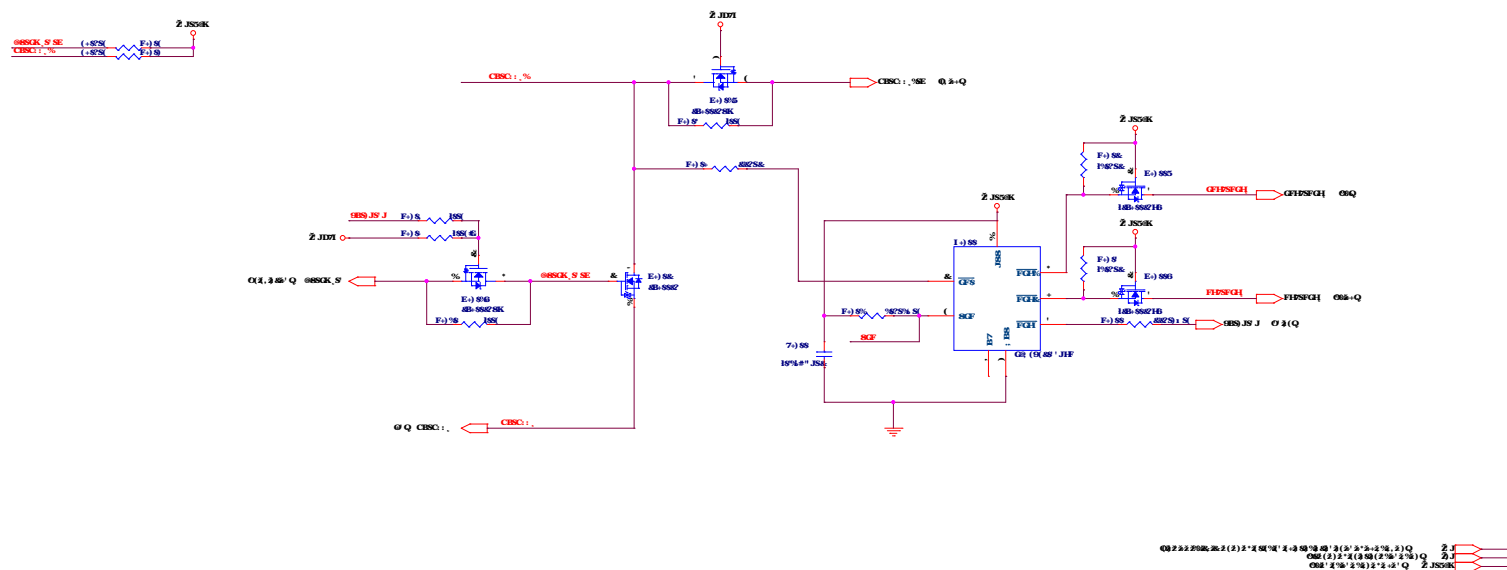


	Platform ID		CCG5 Dual Port	CCG5/CCG5C Single Port	L0 - DV
	MOD_ID1	MOD_ID2	Description (Dual port)	Description (Single Port)	L1 - VDD0/8
Notebooks	L8	L7	Reserved	DRP+USB on 1-port with No MUX	L2 - 2* VDD0/8
	L7	L7	DRP+DP+ AR TBT on 2-ports	DRP+DP on 1-port with ANX MUX	L3 - 3* VDD0/8
	L6	L7	DRP+DP on 2-ports	DRP+DP+TR TBT on 1-port	L4 - 4* VDD0/8
	L5	L7	DRP+DP on 2-ports with ANX MUX	DRP+DP+AR TBT on 1-port (700, 800 Series)	L5 - 5* VDD0/8
	L4	L7	Malta	DRP+DP on 1-port with Parade MUX	L6 - 6* VDD0/8
	L3	L7	DRP+DP+TR TBT on 2-ports	DRP+DP on 1-port (600/700 Series) - AMD SKU1	L7 - 7* VDD0/8
	L2	L7	Reserved	DRP+DP on 1-port (600/700 Series) - AMD SKU2	L8 = VDD0
	L1	L7	Reserved	DRP+DP on 1-port (600/700 Series) - AMD SKU3	
	L0	L7	Reserved	DRP+DP on 1-port with Parade MUX - 2019 5400 G6	

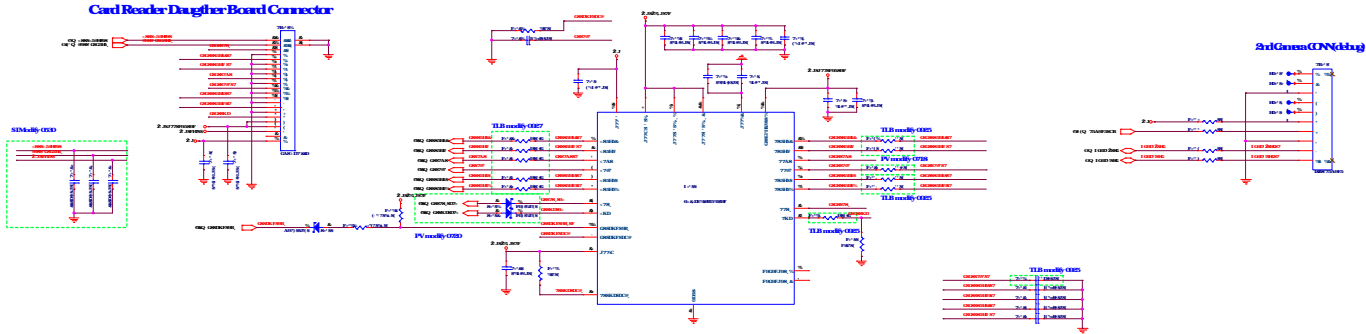




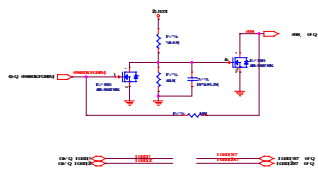




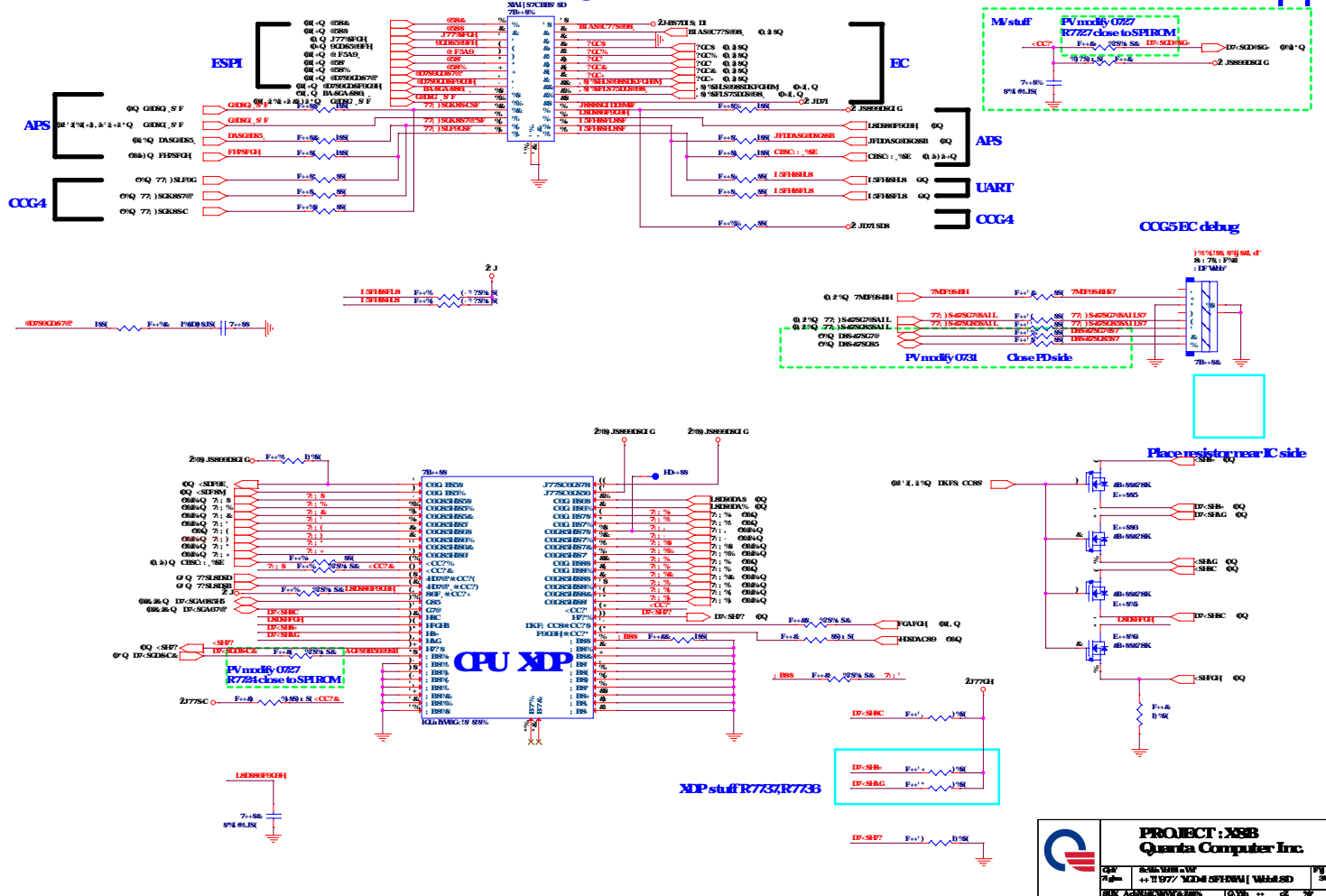
# Card Reader Daughter Board Connector



# USB ID Daughter Board Connector

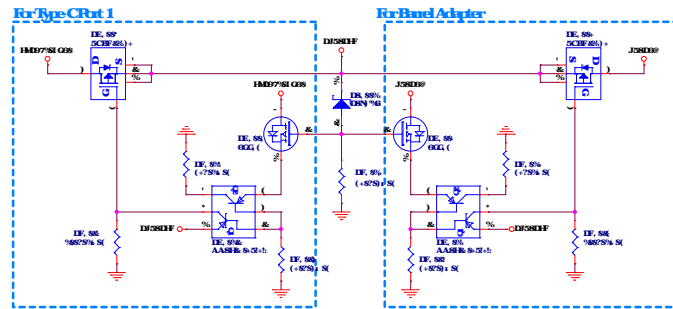
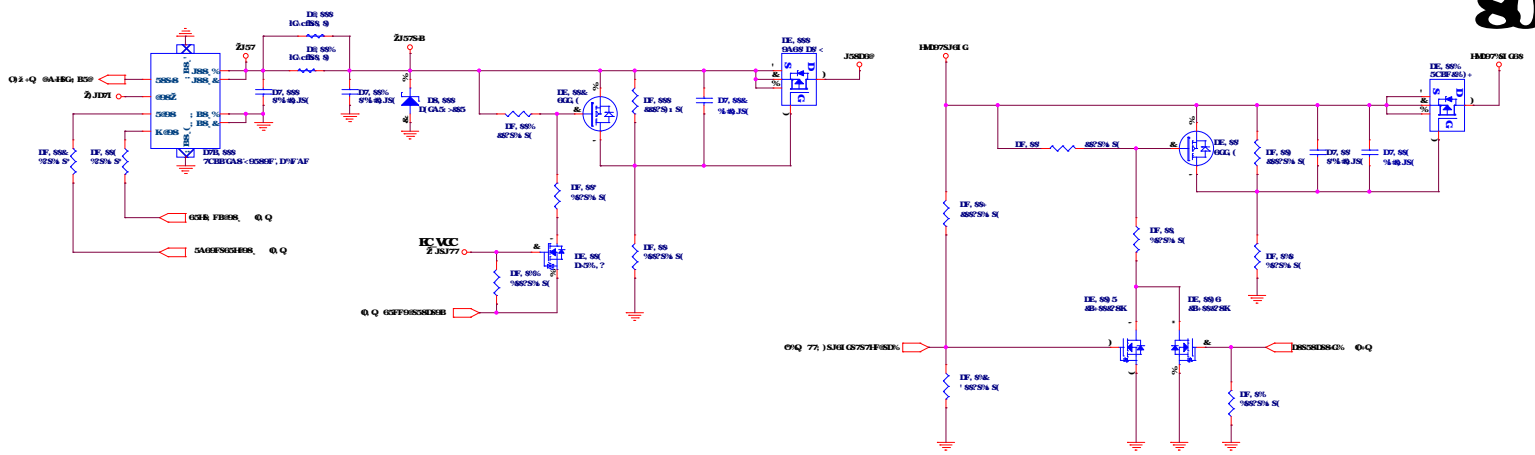


# ESPI+EC+APS debug common MB



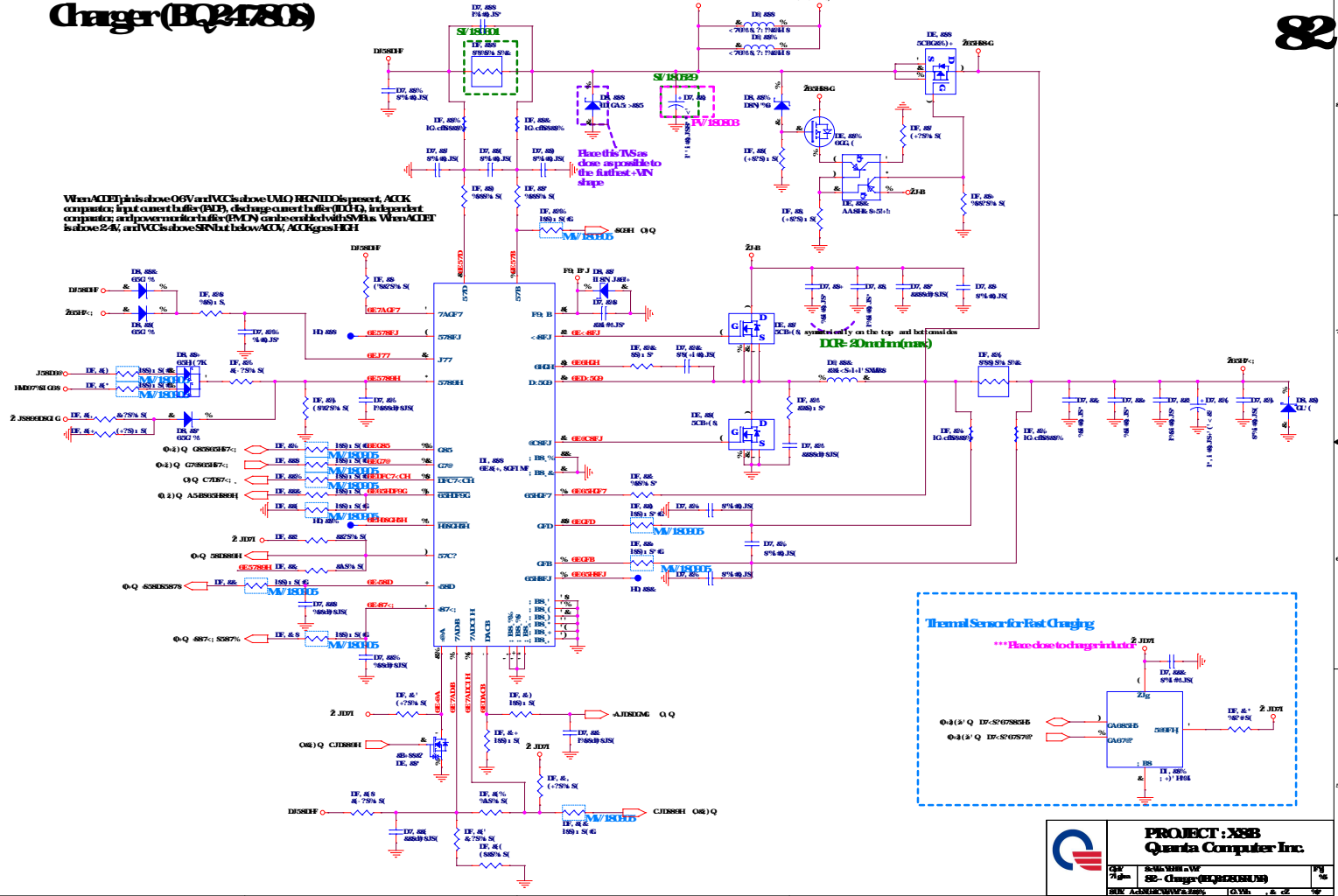


	<b>PROJECT : XSSB</b>	
	<b>Quanta Computer Inc.</b>	
	<small>DATE</small> 7/2/01	<small>BY</small> S
<small>REVISION</small> 25- IN REVISION 200203		<small>DATE</small> 2-25-02






When ACDET pin shows 06V and VCC shows 0MQ, RGNDD is present, ACK compares, input current buffer (IB), discharge current buffer (ID), independent compares, and power mode buffer (PMN) can be enabled with VCC. When ACDET is above 2.4V, and VCC is above 5.0V, but below 6.0V, ACK goes HIGH.




PROJECT : X88			
Quanta Computer Inc.			
Q1	Q2	Q3	Q4
Q5	Q6	Q7	Q8
Q9	Q10	Q11	Q12
Q13	Q14	Q15	Q16
Q17	Q18	Q19	Q20
Q21	Q22	Q23	Q24
Q25	Q26	Q27	Q28
Q29	Q30	Q31	Q32
Q33	Q34	Q35	Q36
Q37	Q38	Q39	Q40
Q41	Q42	Q43	Q44
Q45	Q46	Q47	Q48
Q49	Q50	Q51	Q52
Q53	Q54	Q55	Q56
Q57	Q58	Q59	Q60
Q61	Q62	Q63	Q64
Q65	Q66	Q67	Q68
Q69	Q70	Q71	Q72
Q73	Q74	Q75	Q76
Q77	Q78	Q79	Q80
Q81	Q82	Q83	Q84
Q85	Q86	Q87	Q88
Q89	Q90	Q91	Q92
Q93	Q94	Q95	Q96
Q97	Q98	Q99	Q100





**PROJECT : X88**  
**Quanta Computer Inc.**

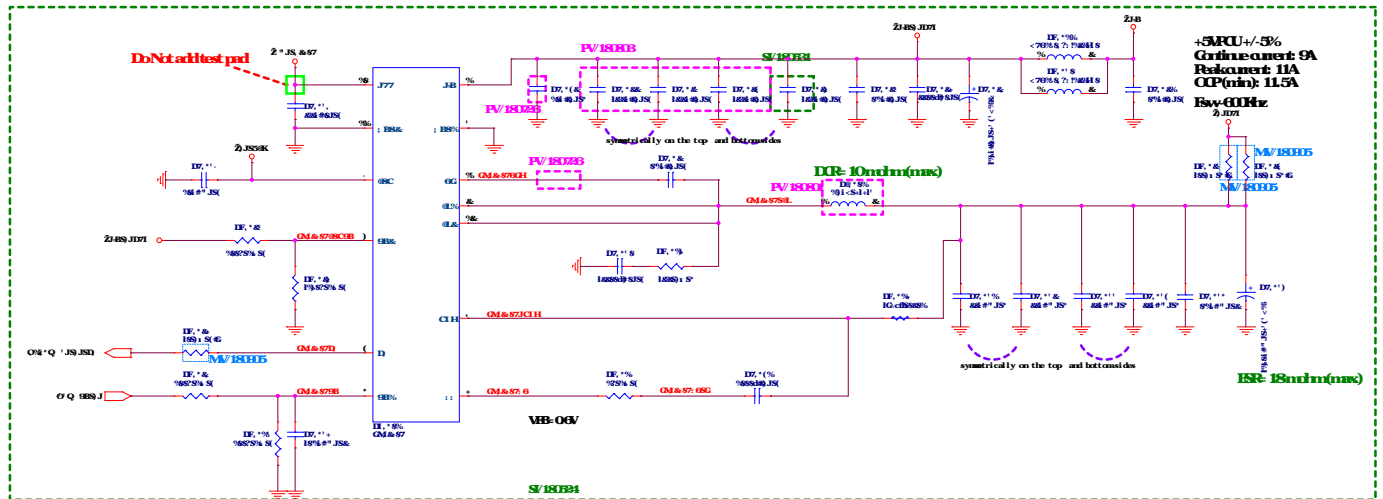
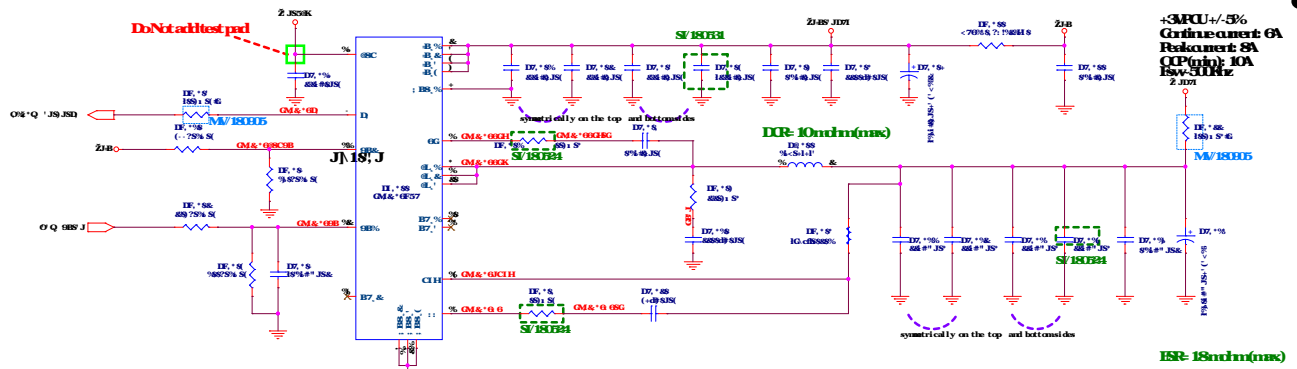
REV	REVISION	BY
01	01- Change (REVISION)	W
REV 1: 2008/05/28		DATE : 08/08/08



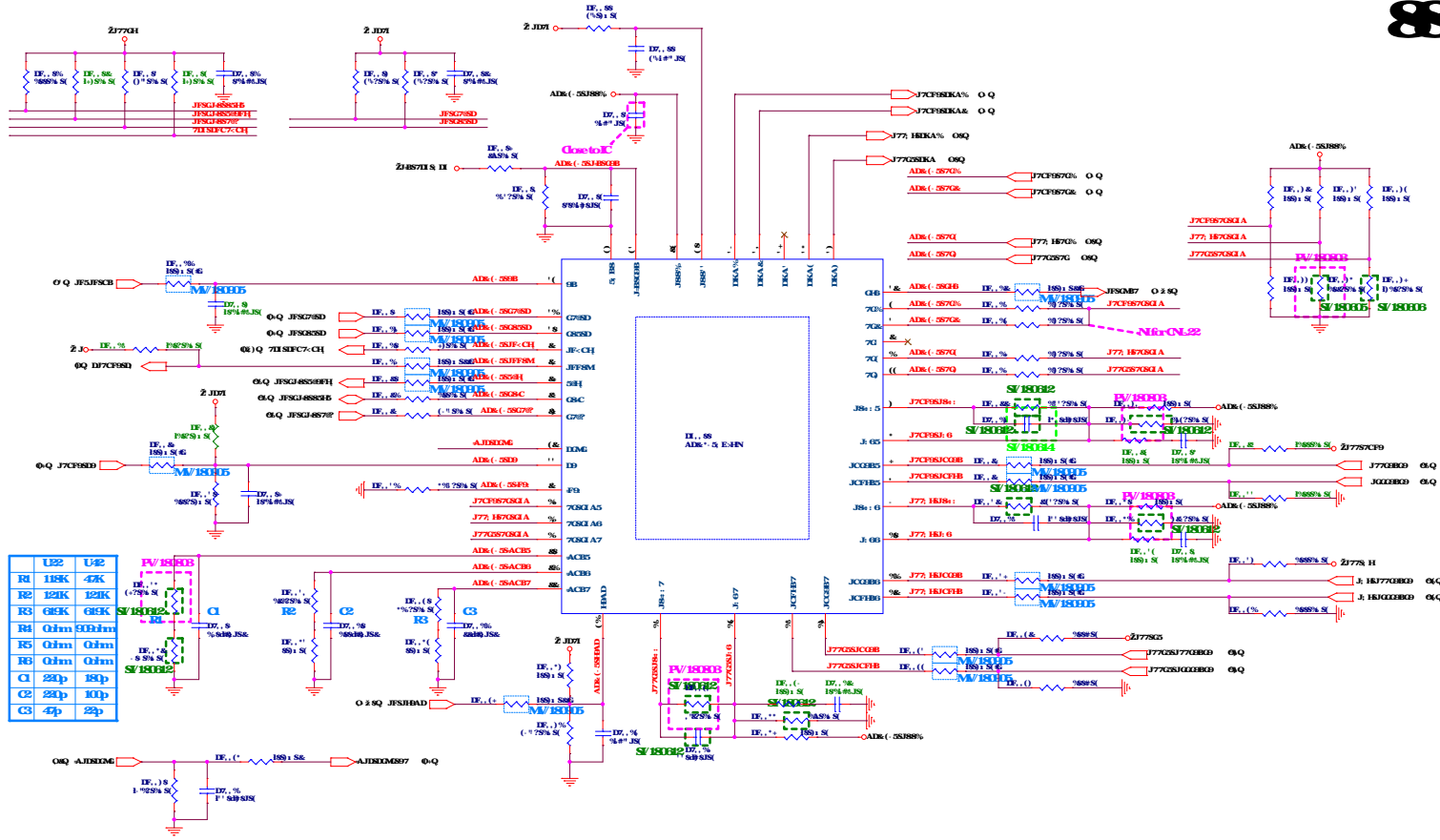
**PROJECT : XBB**  
**Quanta Computer Inc.**

REV	REVISION	BY
7/20	01 - Change (REVISION)	W
REV 7/20/2015	0005	1 of 1

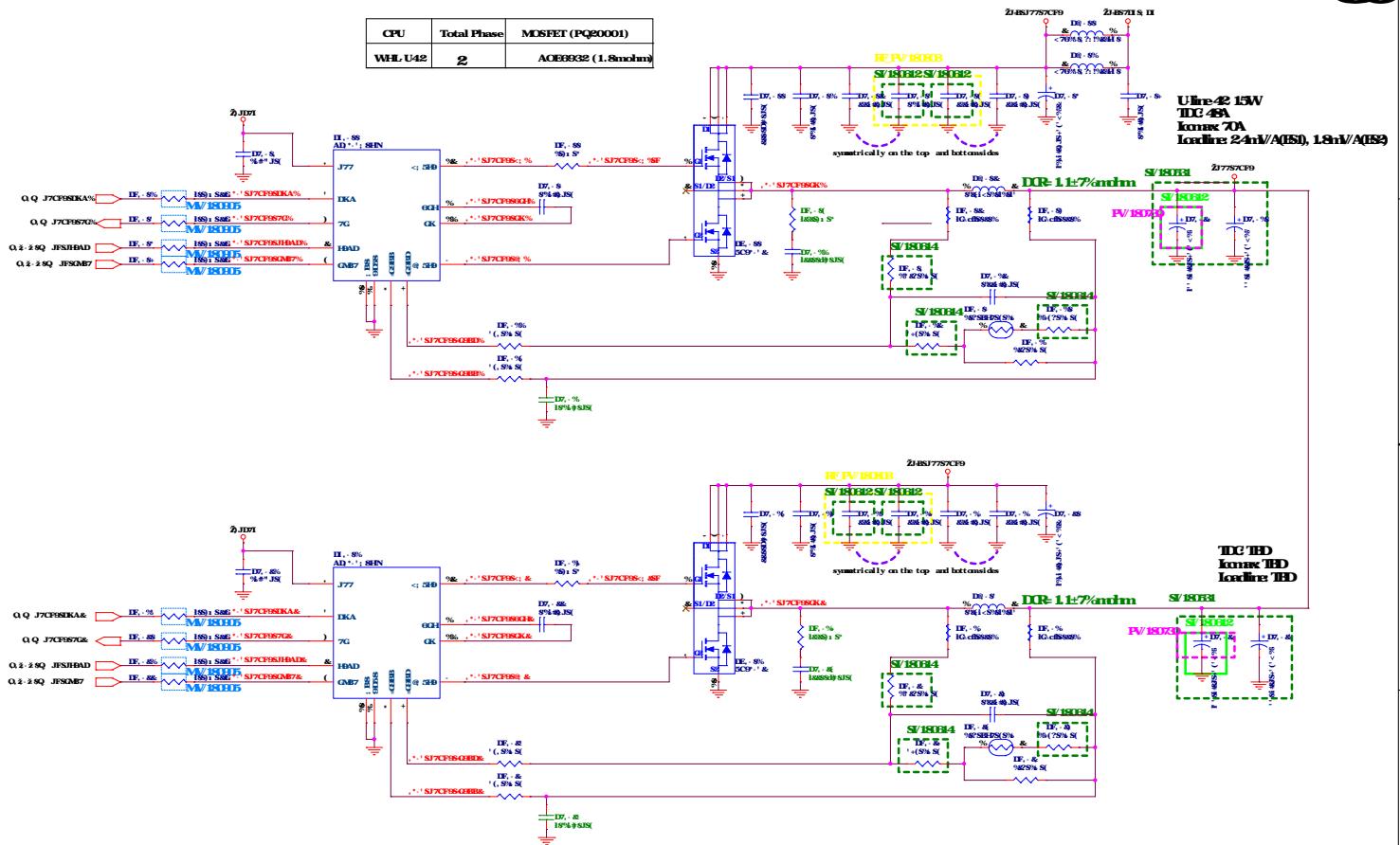




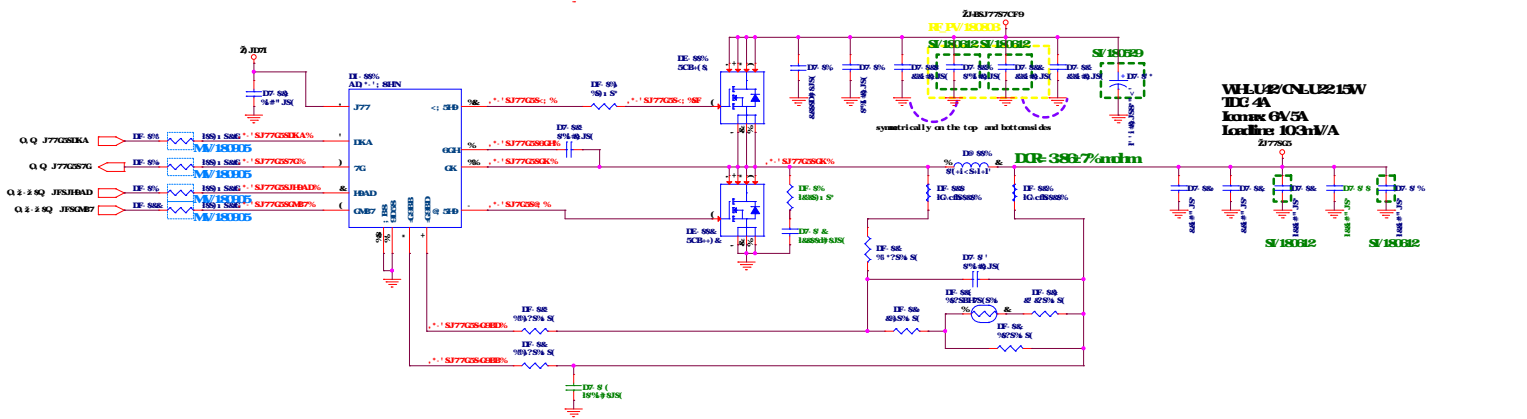
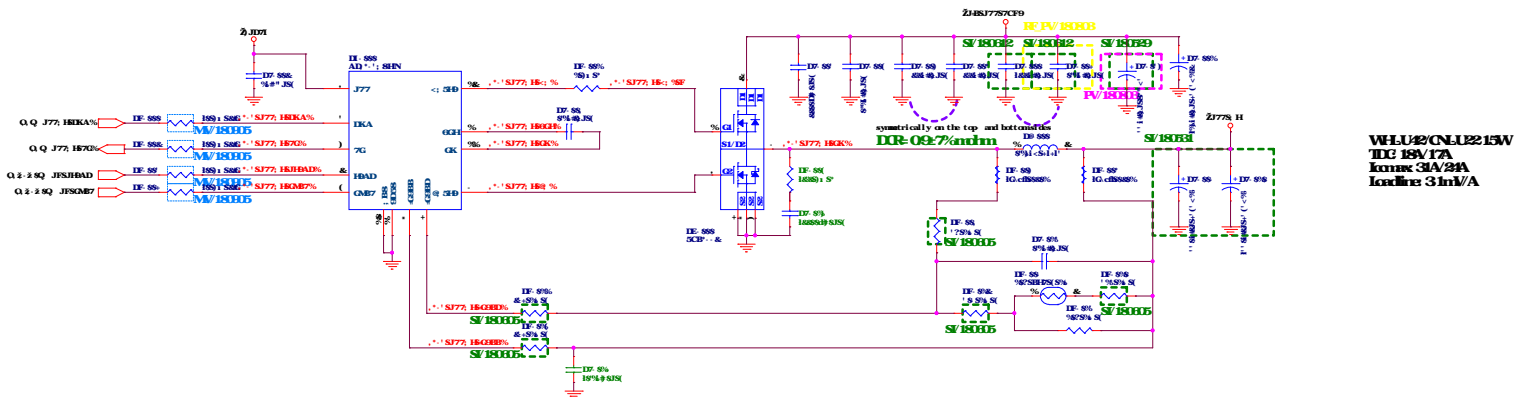





CPU	Total Phase	MOSFET (PQ20001)
WHL U42	2	AOB6032 (1.8mohm)

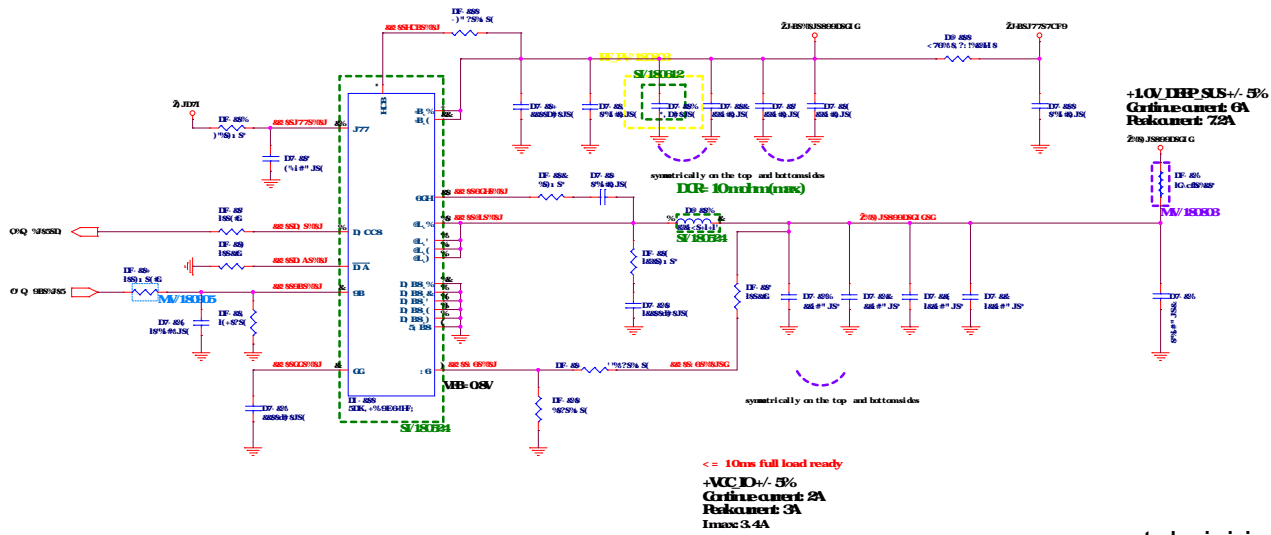


	PROJECT: Megatron	
	Quanta Computer Inc.	
	REV	7/01
	REV	7/01



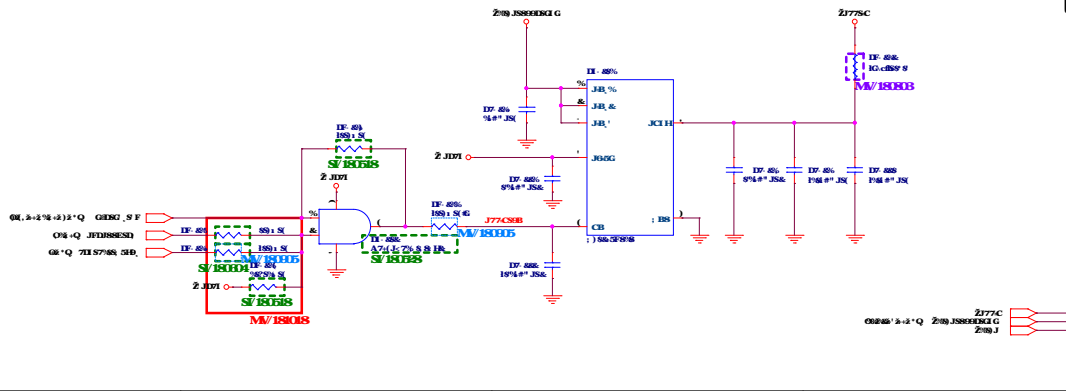


 NBS	<b>PROJECT: 400 SERIES</b>		
	<b>Quanta Computer Inc.</b>		
	QCS 7-10	EXCHG 01 - master page	PI %

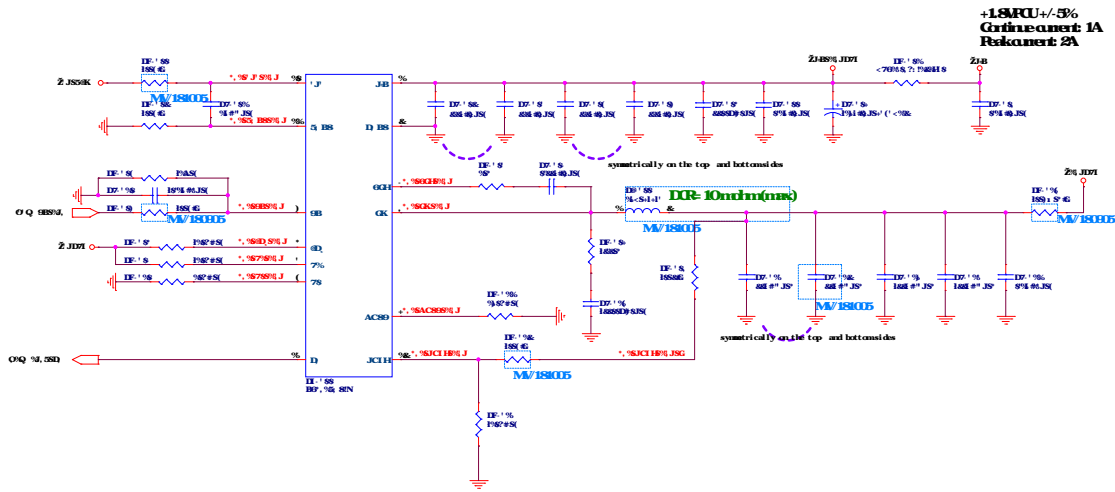


Jc	Flab
81) J	, &
93	, ( ) _
98) J	- ) _
98) J	98b _
98) J	98c _

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PROJECT : X88 Quanta Computer Inc.	
Q1	REVISION
Q2	DATE
Q3	BY




MODE	VR Rail	Resistor to GND (1% Accuracy)
M1	VCCIO	0
M2	PRIMCORE	Float or > 230K
M3	EDRAM/V1.0A/EOPIO	100K
M4	Others	150K

	LP#	C1	C0	VOUT(V)
VCCIO	0	X	X	0
	1	0	0	0.85
	1	0	1	0.875
	1	1	0	0.95
	1	1	1	0.975
VCCPRIM _CORE	0	X	X	0.7
	1	0	0	0.85
	1	0	1	0.9
	1	1	0	0.95
	1	1	1	1.00
EDRAM/ EOPIO/ V1.0A	0	X	X	0
	1	0	0	0.8 (MSM)
	1	0	1	0.95
	1	1	0	1
	1	1	1	1.05
Others (Fixed design only, not allowed for changing on-the-fly)	0	0	0	1.59
	0	0	1	1.99
	0	1	0	2.38
	0	1	1	3.3
	1	0	0	1.2
	1	0	1	1.5
	1	1	0	1.8
	1	1	1	2.5

Note IP# C0 and C1 are pulled up internally

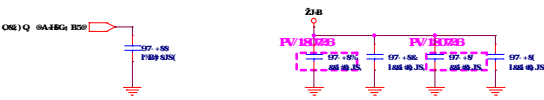
	PROJECT: Bumblebee	
	Quanta Computer Inc.	
	DATE	2014/04/04
	BY	2014/04/04

	<b>PROJECT: 400 SERIES</b>	
	<b>Quanta Computer Inc.</b>	
	DATE: 04-08-2004	BY: 04-08-2004

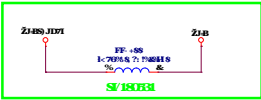





Reserve for EMI & ISEN test





RF Cap



	<b>PROJECT: 400 SERIES</b>	
	<b>Quanta Computer Inc.</b>	
	DATE: 05-08-2005	BY: [signature]




	<b>PROJECT : 400 SERIES</b>	
	<b>Quanta Computer Inc.</b>	
	DATE: 2008-07-10	BY: 2008-07-10


 PT. SAKTI INFORMATIKA	<b>PROJECT : 400 SERIES</b>	
	<b>Quanta Computer Inc.</b>	
	Rev 1.0	Rev 1.0


 NB5	<b>PROJECT: 400 SERIES</b> <b>Quanta Computer Inc.</b>	
	QU 7/10	QUANTUM AI - notebook
	QU 7/10/2015	QU 7/10/2015

 NB5	<b>PROJECT : 400 SERIES</b>		
	<b>Qanta Computer Inc.</b>		
	DATE 7/1/00	REVISION A02- newver.ppt	PI %
REV 12/06/00/2/005		CYS 98.2 W	

 NBS	<b>PROJECT: 400 SERIES</b>	
	<b>Quanta Computer Inc.</b>	
	QTY 740	REVISION K3- new design
REV 1.0000000000000000		DATE: 01.01.00



 NB5	<b>PROJECT : 400 SERIES</b>		
	<b>Qanta Computer Inc.</b>		
	DATE 7/1/00	BY A5 - mureo/pj/00	PL %

	<b>PROJECT : 400 SERIES</b>	
	<b>Quanta Computer Inc.</b>	
	DATE: 2008-11-10	FILE: 400-0000000000
REV: 1.0000000000		DATE: 2008-11-10